

xCORE device

n-bit  
port

UART tx

```
graph LR; subgraph xCORE_device [xCORE device]; n_bit_port[n-bit port]; end; n_bit_port --- UART_tx[UART tx];
```

The diagram illustrates a connection between an xCORE device and a UART tx. The xCORE device is represented by a large white rectangle. Inside it, on the right side, is a smaller gray rectangle labeled 'n-bit port'. A horizontal line extends from the right side of the 'n-bit port' rectangle to the 'UART tx' label, which is positioned outside the xCORE device rectangle.