sliceKIT Core Board (1v1) Connectivity Advisory

The sliceKIT Core Board 1v1 has a problem with connectivity of the Core Board RST# signal to the slice connectors marked STAR and TRIANGLE, and to a circuit that enables or disables the Core Board Flash memory device after boot. This bug results in the following behaviors:

▶ The Ethernet Slice, which uses the RST# signal from the Core Board, will not work in TRIANGLE or STAR slots. It works correctly in the SQUARE and CIRCLE slots. The Ethernet demonstration applications in the xSOFTip library use the TRIANGLE or STAR slots and are not affected by this issue.

▶ Problems may be experienced if trying to boot from flash and then subsequently making use of flash via a program loaded from JTAG (this includes using XFLASH to reflash the device).

If you flash a program that disables flash after booting, you can reflash the device by first running the attached binary to re-enable the flash, before running XFLASH.

This problem will be corrected in the 1V2 sliceKIT Core Board revision due later in 2012.