XCORE Devices Port Maps: xCORE General Purpose, xCORE-USB and xCORE-Analog

This spreadsheet lists which links and ports are bonded out for each device
The rows below list all pin numbers (for pin XnDxx, replace n with the tile number)
For each pin, the function is listed in columns B-G.
Column B lists any link which this pin connects to, columns C-G lists ports.
Column H-T list pin availability for each device; greyed out cells are not available

		Port					General Purpose Device Family							xCORE-USB Device Family			xCORE-Analog Device Family			xCORE-XA Device Fam
							XS1-L4A-64-TQ48 XS1-L6A-64-TQ48 XS1-L8A-64-TQ48	XS1-L6A-64-LQ64 XS1-L8A-64-LQ64	XS1-L6A-64-TQ128 XS1-L8A-64-TQ128	XS1-L8A-128-QF124 XS1-L10A-128-QF124 XS1-L12A-128-QF124 XS1-L16A-128-QF124		XS1-L8A-128-FB324 XS1-L10A-128-FB324 XS1-L12A-128-FB324 XS1-L16A-128-FB324		XS1-U6A-64-FB96 XS1-U8A-64-FB96	XS1-U8A-128-FB217 XS1-U10A-128-FB217 XS1-U12A-128-FB217 XS1-U16A-128-FB217		XS1-A6A-64-FB96 XS1-A8A-64-FB96	XS1-A8A-128-FB217 XS1-A10A-128-FB217 XS1-A12A-128-FB217 XS1-A16A-128-FB217		XS1-XA8A-512-FB265 XS1-XAU8A-512-FB265 XS1-XA8A-1024-FB265 XS1-XAU8A-1024-FB265
Pin	**Link**	**1bit**	**4bit**	**8bit**	**16bit**	**32bit**	Tile 0	Tile 0	Tile 0	Tile 0	Tile 1	Tile 0	Tile 1	Tile 0	Tile 0	Tile 1	Tile 0	Tile 0	Tile 1	Tile 0
XnD00		P1A0																		
XnD01	LA4o	P1B0																		
XnD02	LA3o		P4A0	P8A0	P16A0	P32A20														
XnD03	LA2o		P4A1	P8A1	P16A1	P32A21														
XnD04	LA1o		P4B0	P8A2	P16A2	P32A22														
XnD05	LA0o		P4B1	P8A3	P16A3	P32A23														
XnD06	LA0i		P4B2	P8A4	P16A4	P32A24														
XnD07	LA1i		P4B3	P8A5	P16A5	P32A25														
XnD08	LA2i		P4A2	P8A6	P16A6	P32A26														
XnD09	LA3i		P4A3	P8A7	P16A7	P32A27														
XnD10	LA4i	P1C0																		
XnD11		P1D0																		
XnD12		P1E0																		
XnD13	LB4o	P1F0																		
XnD14	LB3o		P4C0	P8B0	P16A8	P32A28														
XnD15	LB2o		P4C1	P8B1	P16A9	P32A29														
XnD16	LB1o		P4D0	P8B2	P16A10															
XnD17	LB0o		P4D1	P8B3	P16A11															
XnD18	LB0i		P4D2	P8B4	P16A12															
XnD19	LB1i		P4D3	P8B5	P16A13															
XnD20	LB2i		P4C2	P8B6	P16A14	P32A30														
XnD21	LB3i		P4C3	P8B7	P16A15	P32A31														
XnD22	LB4i	P1G0																		
XnD23		P1H0																		
XnD24		P1I0												Sample	Sample					
XnD25		P1J0																		
XnD26			P4E0	P8C0	P16B0															
XnD27			P4E1	P8C1	P16B1															
XnD28			P4F0	P8C2	P16B2															
XnD29			P4F1	P8C3	P16B3															
XnD30			P4F2	P8C4	P16B4															
XnD31			P4F3	P8C5	P16B5															
XnD32			P4E2	P8C6	P16B6															
XnD33			P4E3	P8C7	P16B7															
XnD34		P1K0																		
XnD35		P1L0																		
XnD36		P1M0		P8D0	P16B8															
XnD37		P1N0		P8D1	P16B9															
XnD38		P1O0		P8D2	P16B10															
XnD39		P1P0		P8D3	P16B11															
XnD40				P8D4	P16B12															
XnD41				P8D5	P16B13															
XnD42				P8D6	P16B14															
XnD43				P8D7	P16B15									Wake	Wake		Wake	Wake		
XnD49	LC4o					P32A0														
XnD50	LC3o					P32A1														
XnD51	LC2o					P32A2														
XnD52	LC1o					P32A3														
XnD53	LC0o					P32A4														
XnD54	LC0i					P32A5														
XnD55	LC1i					P32A6														
XnD56	LC2i					P32A7														
XnD57	LC3i					P32A8														
XnD58	LC4i					P32A9														
XnD61	LD4o					P32A10														
XnD62	LD3o					P32A11														
XnD63	LD2o					P32A12														
XnD64	LD1o					P32A13														
XnD65	LD0o					P32A14														
XnD66	LD0i					P32A15														
XnD67	LD1i					P32A16														
XnD68	LD2i					P32A17														
XnD69	LD3i					P32A18														
XnD70	LD4i					P32A19														

ADC0																		Use ARM ADCs instead
ADC1																		
ADC2																		
ADC3																		
ADC4																		
ADC5																		
ADC6																		
ADC7																		