

PCB Manufacturing Notes

General Info

Board dimensions – 50mm x 30mm
Number of layers – 2
Smallest hole – 0.35mm
Number of holes – Approx 140

Stackup

Stackup is to be as follows:

Layer	Copper Weight
Layer 01 (Top)	0.5oz
Layer 02 (Bottom)	0.5oz

Finished board thickness to be 1.6mm ± 0.1mm

Impedance Control

Not required.

Copper Thieving/Balancing

The supplier may add copper thieving/balancing if required.

Finish

A.) Conductive finish

Plating to be immersion silver (preferred) or HASL.

B.) Soldermask

Liquid photo imageable soldermask (green). Pads have not been oversized.
Supplier should oversize soldermask on pads to suit process.

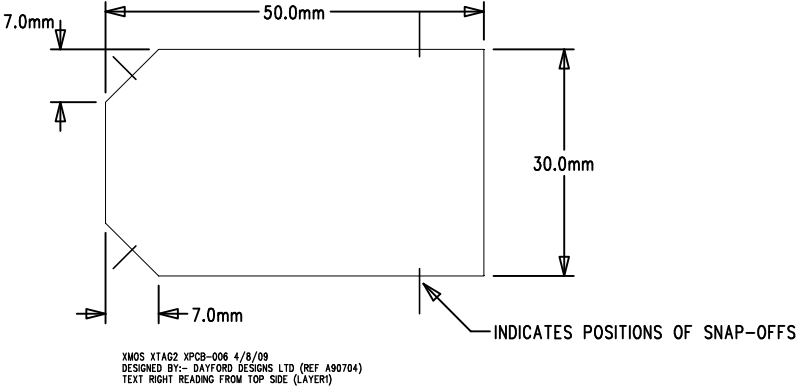
C.) Silkscreen

Colour white. Supplier should remove any silkscreen which overhangs pads.

Drill Data

Drill data is in Excellon format, metric (000.000), suppress leading zeros, absolute coordinates.

Hole size is finished size.

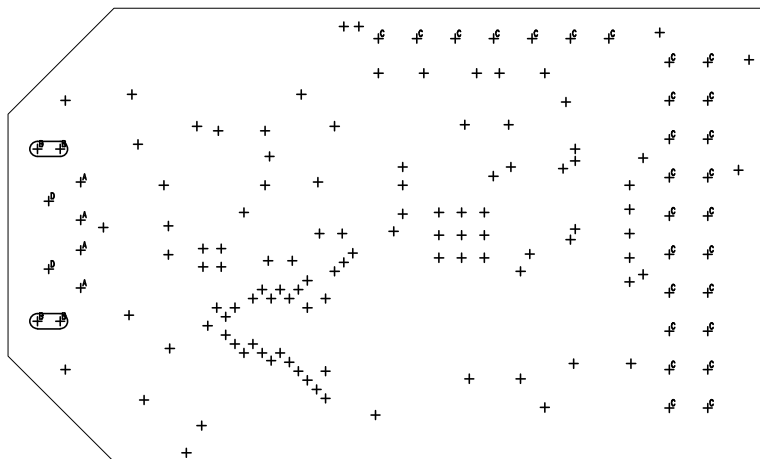


XMOS

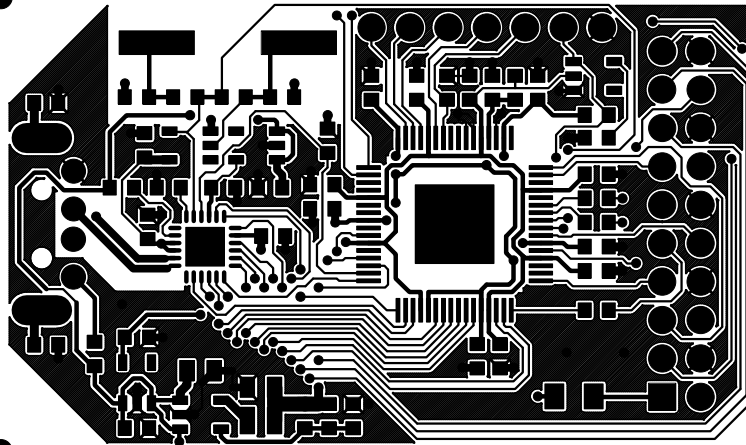
Project Name
XPCB-006-XTAG-L1

Sheet	Date	Revision
A4	Aug 4th 2009	A

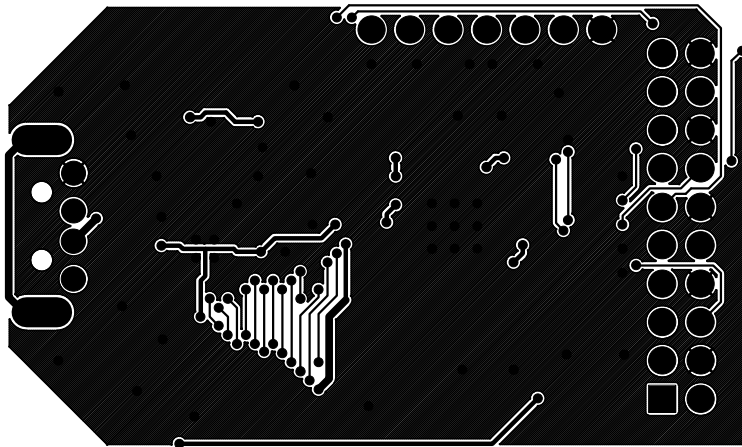
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SIZE	QTY	SYM	PLATED	TOL
0.3	107	+	YES	+/-
0.95	4	x ^A	YES	+/-
1 x 2.5	2	+	YES	+/-
1	27	+	YES	+/-
1.1	2	+	NO	+/-



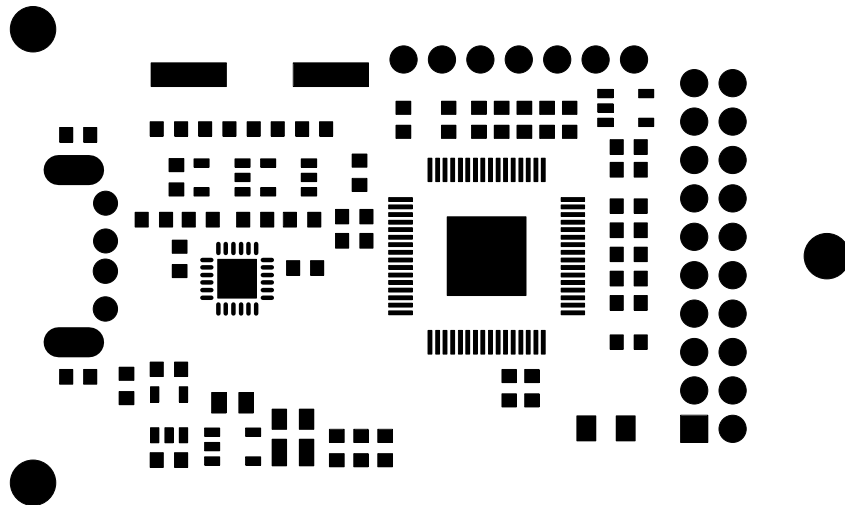
XMOS XTAG2 XPCB-006 4/8/09
DESIGNED BY:- DAYFORD DESIGNS LTD (REF A90704)
TEXT RIGHT READING FROM TOP SIDE (LAYER1)
PCB COPPER LAYER 1



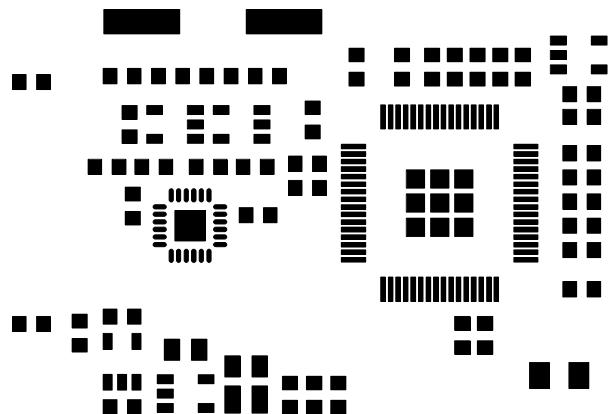
XMOS XTAG2 XPCB-006 4/8/09
DESIGNED BY:- DAYFORD DESIGNS LTD (REF A90704)
TEXT RIGHT READING FROM TOP SIDE (LAYER1)
PCB COPPER LAYER 2



XMOS XTAG2 XPCB-006 4/8/09
DESIGNED BY:- DAYFORD DESIGNS LTD (REF A90704)
TEXT RIGHT READING FROM TOP SIDE (LAYER1)
SOLDER MASK BOTTOM



XMOS XTAG2 XPCB-006 4/8/09
DESIGNED BY:- DAYFORD DESIGNS LTD (REF A90704)
TEXT RIGHT READING FROM TOP SIDE (LAYER1)
SOLDER MASK TOP



XMOS XTAG2 XPCB-006 4/8/09
DESIGNED BY:- DAYFORD DESIGNS LTD (REF A90704)
TEXT RIGHT READING FROM TOP SIDE (LAYER1)
PASTE MASK TOP



X MOS XTAG2 XPCB-006 4/8/09
 DESIGNED BY:- DAYFORD DESIGNS LTD (REF A90704)
 TEXT RIGHT READING FROM TOP SIDE (LAYER1)
 SILKSCREEN TOP