

xCORE device

n-bit
port

UART rx

```
graph LR; subgraph xCORE_device [xCORE device]; n_bit_port[n-bit port]; end; n_bit_port --- UART_rx[UART rx];
```

The diagram illustrates a connection between an xCORE device and a UART receiver. The xCORE device is represented by a large rectangle on the left. Inside its right edge is a smaller, shaded vertical rectangle labeled 'n-bit port'. A horizontal line extends from the right side of this 'n-bit port' rectangle, crossing the boundary of the xCORE device and continuing to the right, where it is labeled 'UART rx'.