

# xCORE device

1-bit  
port

UART



```
graph LR; subgraph xCORE_device [xCORE device]; direction TB; P[1-bit port]; end; P --- UART[UART];
```

The diagram illustrates a connection between an xCORE device and a UART interface. A rectangular box labeled '1-bit port' is positioned inside the 'xCORE device' boundary. A horizontal line extends from the right side of this box, crossing the boundary of the device and connecting to the label 'UART'.